In re Patent Application of:
LEAMING
Serial No. **10/829,008**
Filing Date: **April 21, 2004**
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In the Claims:

This listing of claims replaces all prior versions and listing of claims in the application.

1. (Currently amended) An integrated circuit for a smart card comprising:

at least one data terminal for providing communications with a host device over a system bus; and

a processor ~~for~~ configured to

~~providing~~ provide an attachment signal on the at least one data terminal for recognition by the host device,

~~cooperating~~ cooperate with the host device to perform an enumeration based upon at least one default descriptor, ~~and~~

receive information from the host device regarding an allocation of system bus bandwidth to other devices communicating with the host device over the system bus,

~~selectively removing~~ remove the attachment signal from the at least one data terminal and thereafter again ~~providing~~ provide the attachment signal on said at least one data terminal based upon the information regarding the allocation of system bus bandwidth to other devices, and

~~cooperating~~ cooperate with the host device to perform a new enumeration based upon at least one

alternate descriptor ~~based upon allocations of system bus bandwidth to other devices communicating with the host device over the system bus~~.

2. (Currently amended) The integrated circuit of Claim 1 further comprising at least one power terminal connected to said processor, and wherein said processor ~~receives~~ is configured to receive power via said at least one power terminal during removal of the attachment signal.

Claims 3 and 4 (cancelled).

5. (Currently amended) The integrated circuit of Claim 1 wherein said processor ~~monitors~~ is configured to monitor communications with the host device during removal of the attachment signal.

6. (Original) The integrated circuit of Claim 1 wherein the at least one alternate descriptor comprises at least one device descriptor.

7. (Original) The integrated circuit of Claim 1 wherein the at least one alternate descriptor comprises at least one configuration descriptor.

8. (Original) The integrated circuit of Claim 1 wherein the at least one alternate descriptor comprises at least one interface descriptor.

9. (Original) The integrated circuit of Claim 1 wherein the at least one alternate descriptor comprises at least one endpoint descriptor.

10. (Original) The integrated circuit of Claim 1 wherein said at least one data terminal comprises first and second data terminals for differential data signals.

11. (Original) The integrated circuit of Claim 1 further comprising a USB transceiver connected between said processor and said at least one data terminal.

12. (Currently amended) A smart card comprising:

a smart card body; and

an integrated circuit carried by said smart card body and comprising

at least one data terminal for providing communications with a host device over a system bus, and

a processor ~~for~~ configured to

~~providing~~ provide an attachment signal on the at least one data terminal for recognition by the

host device,

~~cooperating~~ cooperate with the host device to perform an enumeration based upon at least one default descriptor, ~~and~~

receive information from the host device regarding an allocation of system bus bandwidth to other devices communicating with the host device over the system bus,

~~selectively removing~~ remove the attachment signal from the at least one data terminal and thereafter again ~~providing~~ provide the attachment signal on said at least one data terminal based upon the information regarding the allocation of system bus bandwidth to other devices, and

~~cooperating~~ cooperate with the host device to perform a new enumeration based upon at least one alternate descriptor ~~based upon allocations of system bus bandwidth to other devices communicating with the host device over the system bus~~.

13. (Currently amended) The smart card of Claim 12 wherein said integrated circuit further comprises at least one power terminal connected to said processor, and wherein said processor ~~receives~~ is configured to receive power via said at least one power terminal during removal of the attachment signal.

Claims 14 and 15 (cancelled).

16. (Currently amended) The smart card of Claim 12 wherein said processor ~~monitors~~ is configured to monitor communications with the host device during removal of the attachment signal.

17. (Original) The smart card of Claim 12 wherein the at least one alternate descriptor comprises at least one device descriptor.

18. (Original) The smart card of Claim 12 wherein the at least one alternate descriptor comprises at least one configuration descriptor.

19. (Original) The smart card of Claim 12 wherein the at least one alternate descriptor comprises at least one interface descriptor.

20. (Original) The smart card of Claim 12 wherein the at least one alternate descriptor comprises at least one endpoint descriptor.

21. (Original) The smart card of Claim 12 wherein said at least one data terminal comprises first and second data terminals for differential data signals.

22. (Original) The smart card of Claim 12 further comprising a USB transceiver connected between said processor and said at least one data terminal.

23. (Currently amended) A smart card system comprising:

a host device and associated system bus;

a smart card adapter connected to said host device via said system bus; and

a smart card to be read by said smart card adapter and comprising a smart card body and an integrated circuit carried by said smart card body, said integrated circuit comprising

at least one data terminal for providing communications with a host device over said system bus, and

a processor ~~for~~ configured to

~~providing~~ provide an attachment signal on the at least one data terminal for recognition by said host device,

~~cooperating~~ cooperate with said host device to perform an enumeration based upon at least one default descriptor, ~~and~~

~~selectively removing~~ remove the attachment signal from the at least one data terminal and thereafter again ~~providing~~ provide the attachment signal on said at least one data terminal based upon the information regarding the allocation of system bus bandwidth to other devices, and

~~cooperating~~ cooperate with said host device to perform a new enumeration based upon at least one alternate descriptor ~~based upon allocations of system bus bandwidth to other devices communicating with said host device over said system bus~~.

24. (Currently amended) The smart card system of Claim 23 wherein said integrated circuit further comprises at least one power terminal connected to said processor, and wherein said processor ~~receives~~ is configured to receive power via said at least one power terminal during removal of the attachment signal.

25. (Original) The smart card system of Claim 23 wherein the system event comprises a system utilization metric exceeding a threshold.

26. (Original) The smart card system of Claim 23 wherein the system event comprises the occurrence of attempted unauthorized communications.

27. (Original) The smart card system of Claim 23 wherein said processor monitors communications with said host device during removal of the attachment signal.

28. (Original) The smart card system of Claim 23 wherein the at least one alternate descriptor comprises at least one device descriptor.

29. (Original) The smart card system of Claim 23 wherein the at least one alternate descriptor comprises at least one configuration descriptor.

30. (Original) The smart card system of Claim 23 wherein the at least one alternate descriptor comprises at least one interface descriptor.

31. (Original) The smart card system of Claim 23 wherein the at least one alternate descriptor comprises at least one endpoint descriptor.

32. (Original) The smart card system of Claim 23 wherein said at least one data terminal comprises first and second data terminals for differential data signals.

33. (Original) The smart card system of Claim 23 further comprising a USB transceiver connected between said processor and said at least one data terminal.

34. (Currently amended) A method for operating a smart card comprising at least one data terminal, the method comprising:

providing an attachment signal on the at least one data terminal for recognition by a host device over a system bus;

cooperating with the host device over the system bus to perform an enumeration based upon at least one default descriptor; ~~and~~

receiving information from the host device regarding an allocation of system bus bandwidth to other devices communicating with the host device over the system bus,

~~selectively~~ removing the attachment signal from the at least one data terminal and thereafter again providing the attachment signal on the at least one data terminal based upon the information regarding the allocation of system bus bandwidth to other devices; and

cooperating with the host device to perform a new enumeration based upon at least one alternate descriptor ~~based upon allocations of system bus bandwidth to other devices communicating with the host device over the system bus~~.

35. (Original) The method of Claim 34 wherein the smart card further comprises at least one power terminal connected to the processor, and wherein the smart card receives power via the at least one power terminal during removal of the attachment signal.

Claims 36 and 37 (cancelled).

38. (Original) The method of Claim 34 further comprising monitoring communications with the host device during removal of the attachment signal.

39. (Original) The method of Claim 34 wherein the at least one alternate descriptor comprises at least one device descriptor.

40. (Original) The method of Claim 34 wherein the at least one alternate descriptor comprises at least one configuration descriptor.

41. (Original) The method of Claim 34 wherein the at least one alternate descriptor comprises at least one interface descriptor.

42. (Original) The method of Claim 34 wherein the at least one alternate descriptor comprises at least one endpoint descriptor.

43. (Original) The method of Claim 34 wherein the at least one data terminal comprises first and second data terminals for differential data signals.

44. (Original) The method of Claim 34 wherein the smart card operates in a universal serial bus (USB) mode.

Claims 45-50 (cancelled)